

Fig. 1

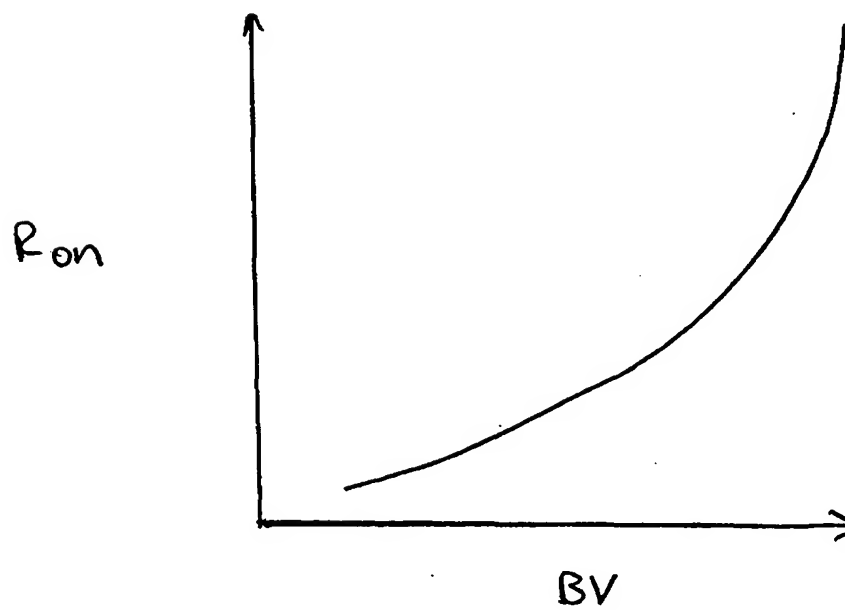


Fig. 2

PRIOR ART

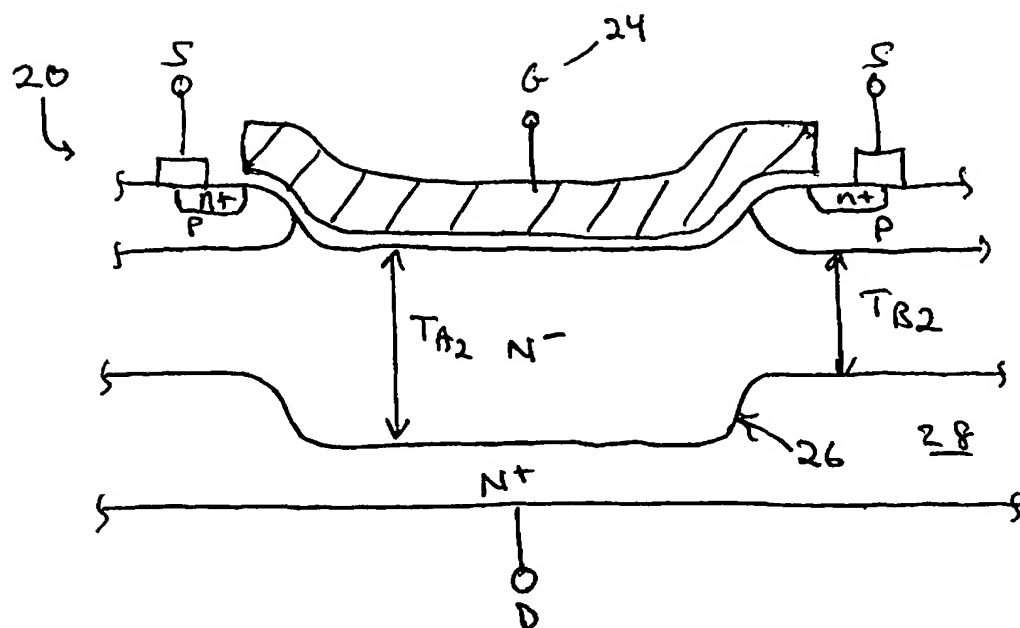


Fig. 3.
PRIOR ART

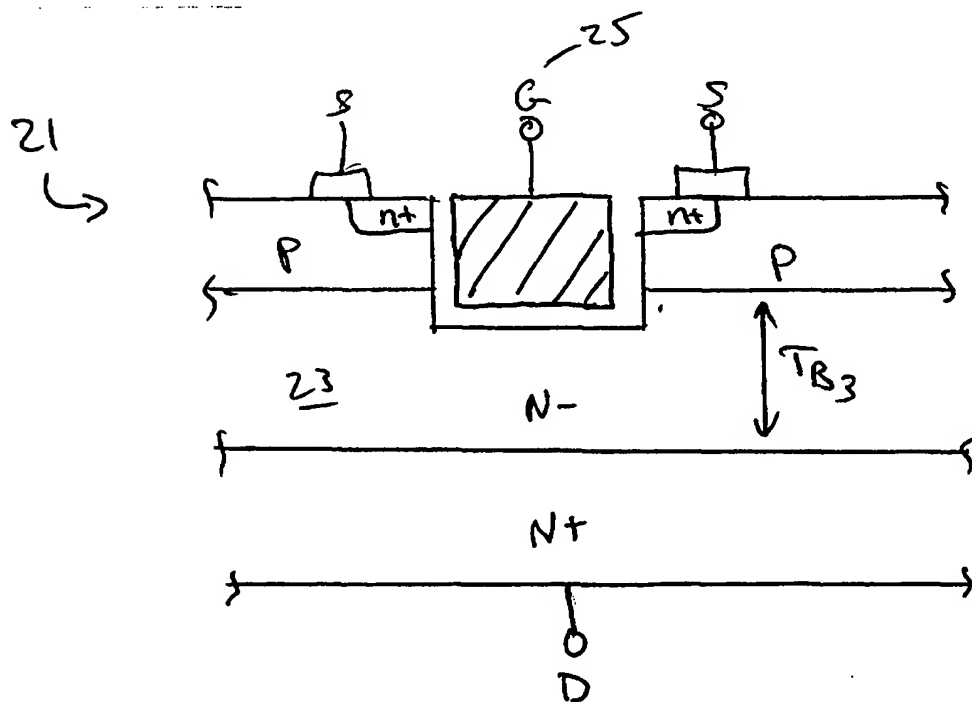


Fig. 4.
PRIOR ART

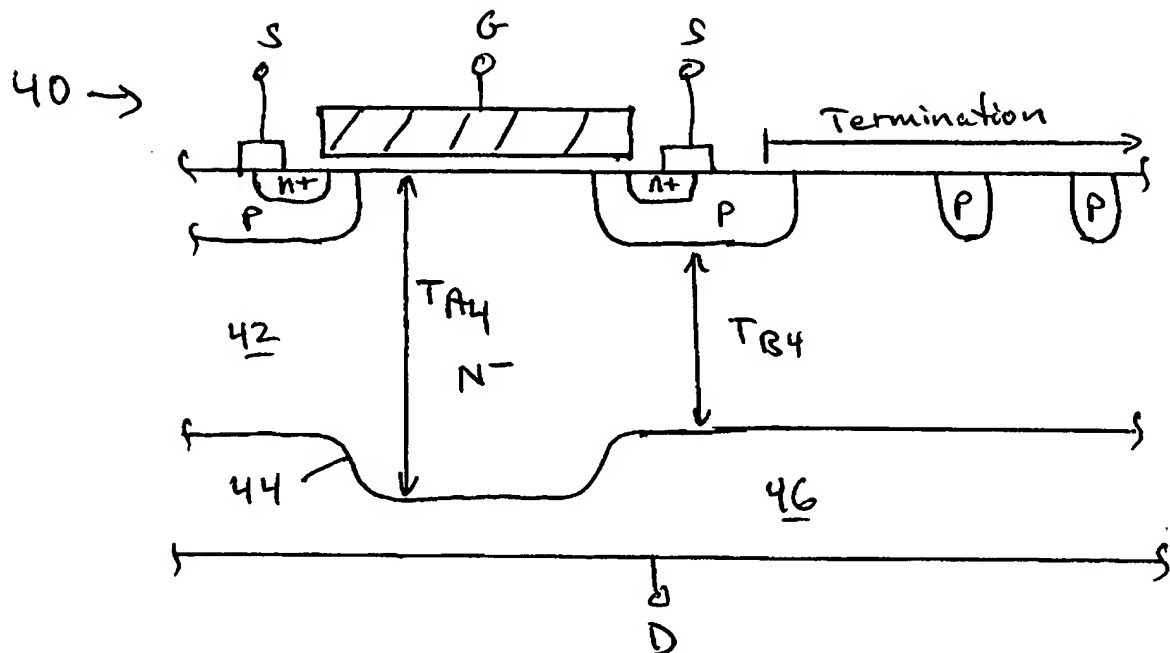


Fig. 5.

PRIOR ART

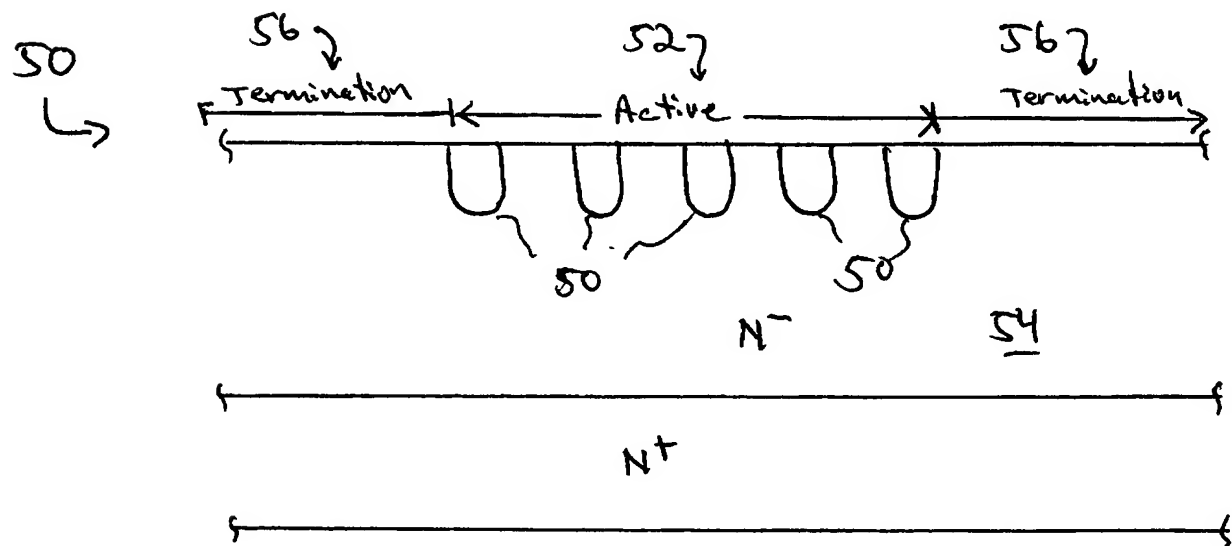


Fig. 6.

PRIOR ART

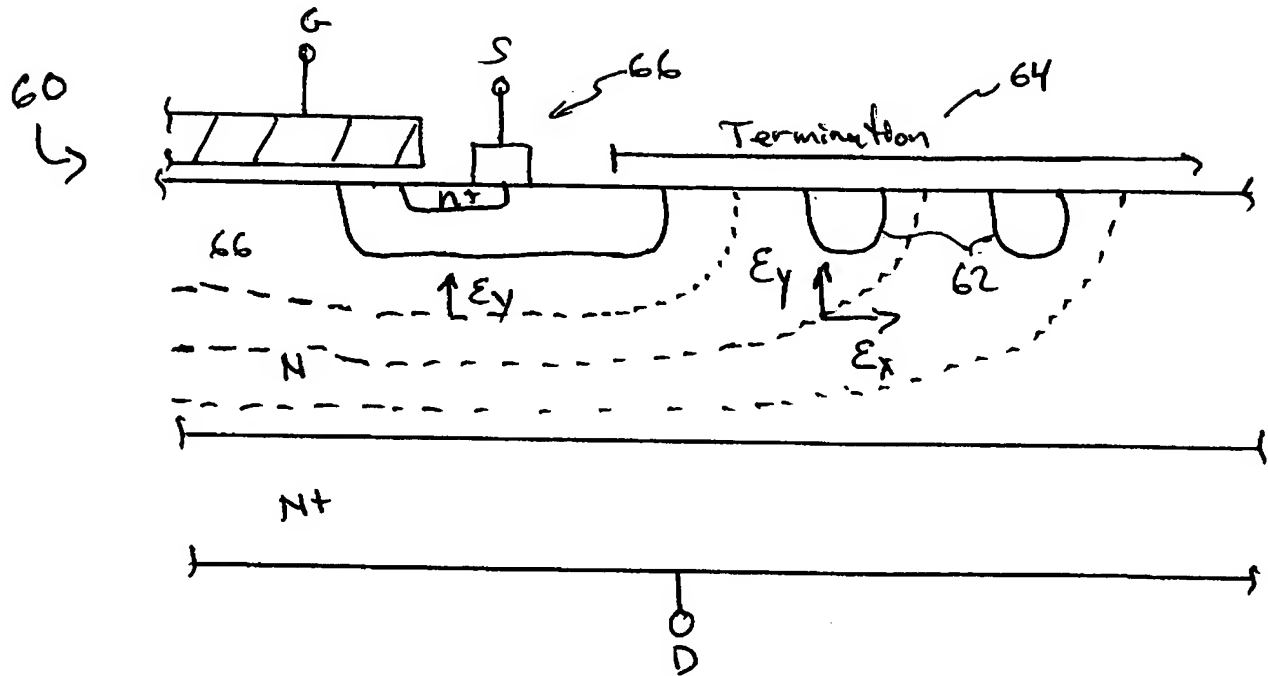


Fig. 7.

PRIOR ART

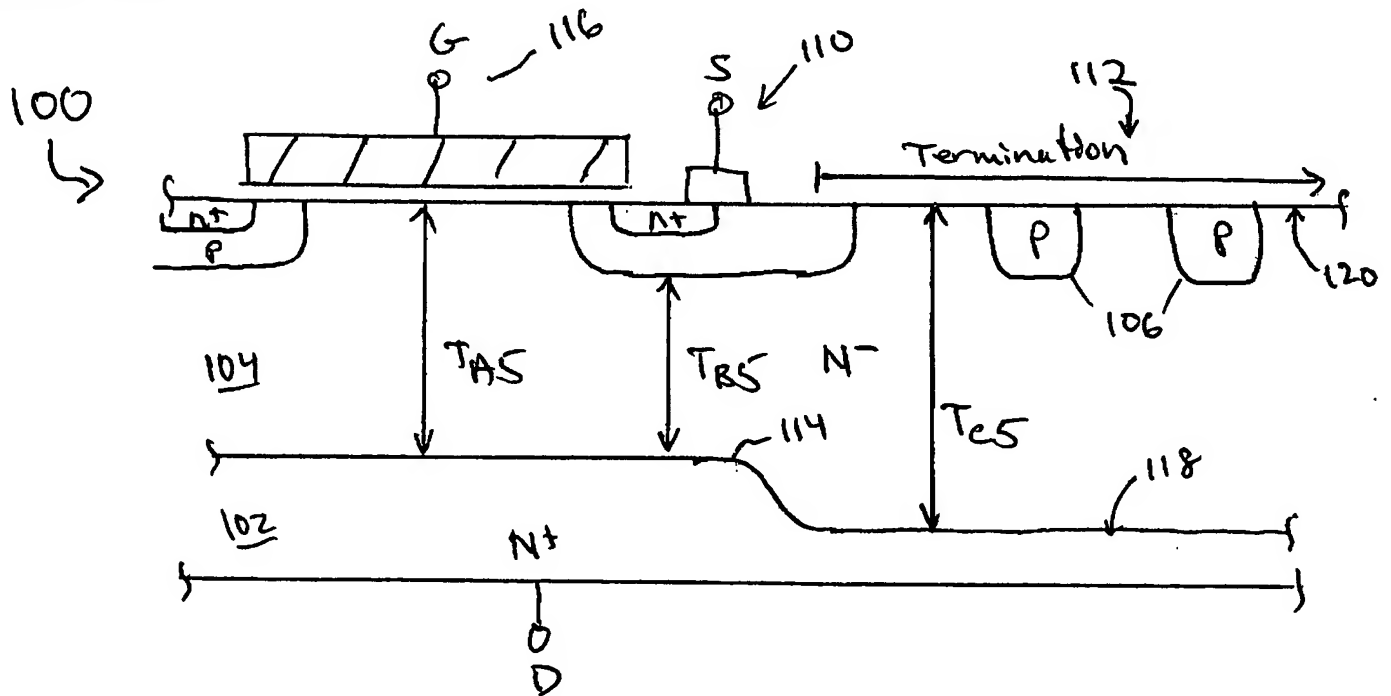
This
Invention:

Fig. 8.

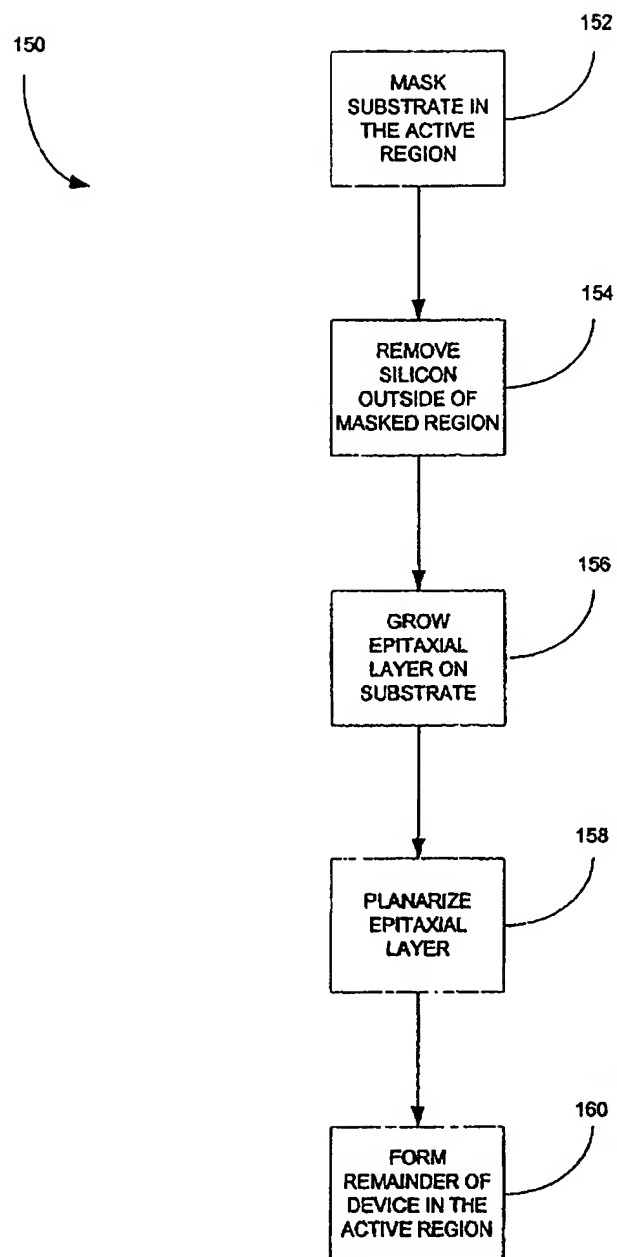


FIG. 9.

350v MESA Transistor BV & Ron*W
N_{min}al pi thickness T_{C6}=35um

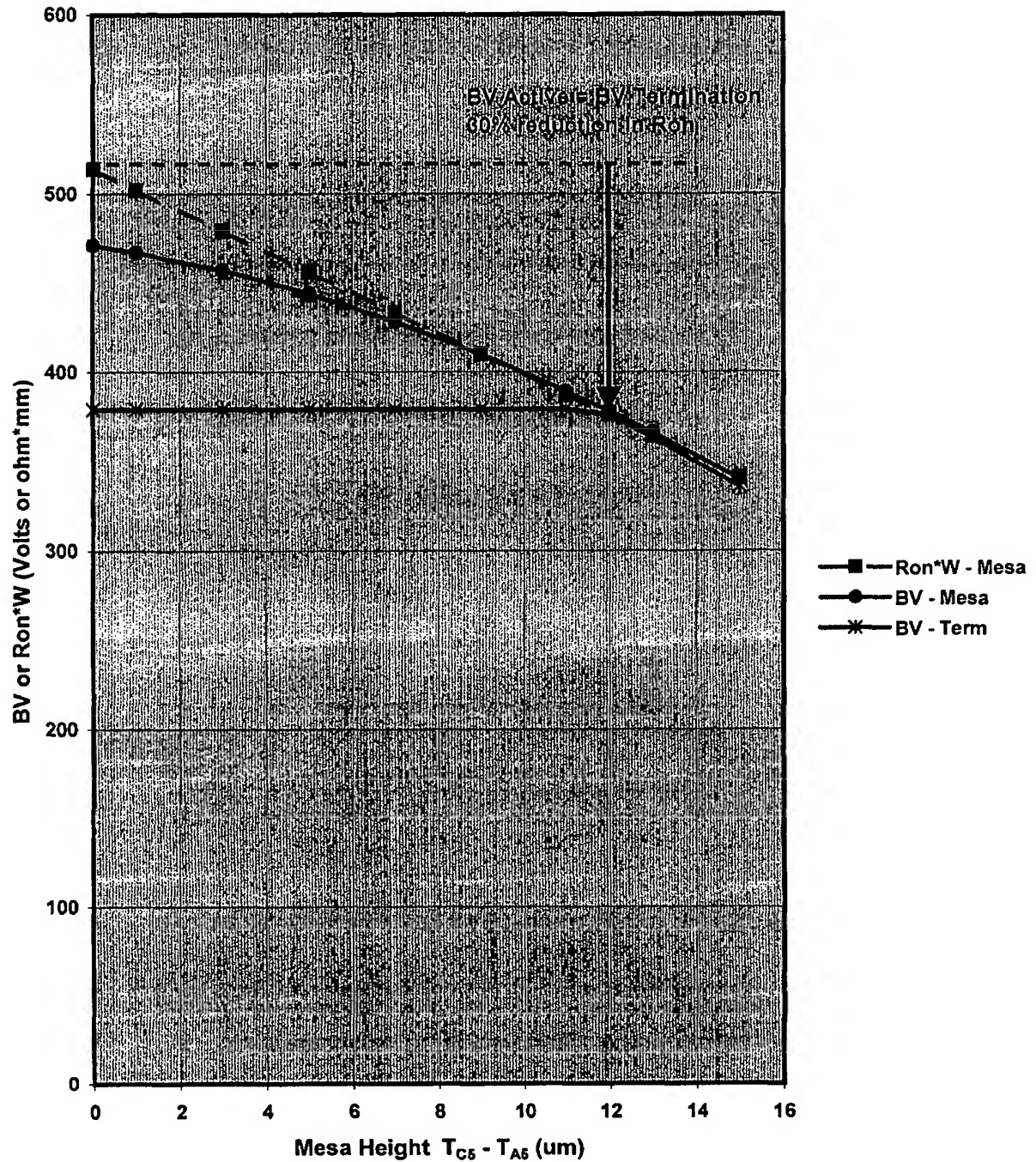


Fig. 10